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Correction to: Simultaneous multiprocessing in a software-defined heterogeneous FPGA

Jose Nunez-Yanez¹ · Sam Amiri¹ · Mohammad Hosseinabady¹ ·
Andrés Rodríguez² · Rafael Asenjo² · Angeles Navarro² · Dario Suarez³ ·
Ruben Gran³

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Correction to: J Supercomput <https://doi.org/10.1007/s11227-018-2367-9>

The presentation of Table 2 was incorrect in the original article. The correct Table 2 is given below. The original article has been corrected.

The original article can be found online at <https://doi.org/10.1007/s11227-018-2367-9>.

✉ Sam Amiri
ma17215@bristol.ac.uk

Jose Nunez-Yanez
j.l.nunez-yanez@bristol.ac.uk

Mohammad Hosseinabady
m.hosseinabady@bristol.ac.uk

Andrés Rodríguez
andres@ac.uma.es

Rafael Asenjo
asenjo@ac.uma.es

Angeles Navarro
angeles@ac.uma.es

Dario Suarez
dario@unizar.es

Ruben Gran
rgran@unizar.es

¹ University of Bristol, Bristol, UK

² Universidad de Málaga, Málaga, Spain

³ Universidad de Zaragoza, Zaragoza, Spain

Table 2 Energy change when the second CPU core is utilised

	AES (%)	HotSpot (%)	GEMM (%)	Nbody (%)
Dynamic with interrupt	− 7.73	+ 22.93	− 1.52	− 1.64
Dynamic without interrupt	− 1.53	+ 2.14	+ 13.64	+ 11.67
LogFit with interrupt	− 19.51	+ 16.54	+ 2.87	+ 6.89
LogFit without interrupt	+ 16.00	+ 10.16	+ 1.12	+ 7.59

Negative values indicate improvement in energy consumption. The lower the better

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